JUN 1 5 2005 Substitute

Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 1 of 1

	Complete if Known
Application Number	10/037,666
Filing Date	January 3, 2002
First Named Inventor	Sriram Vajapeyam
Art Unit	2183
Examiner Name	David I. Huisman
Attomey Docket Numbe	42390P11927

		NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.1					
0071		International Search Report for International Application No. PCT/US02/41551, mailed April 23, 2003 (2 pages).				
M		Friendly, Daniel Holmes, et al., "Putting the Fill Unit toWork: Dynamic Optimizations for Trace Cache Microprocessors", Proceedings of the 31st Annual ACM/IEEE International Symposium on Microarchitecture, pp. 173-181 (November 30, 1998).				
tra	· · · · · · · · · · · · · · · · · · ·	Patel, Sanjay J., et al., "rePLay: A Hardware Framework for Dynamic Optimization", IEEE Transactions On Computers, Vol. 50, No. 6, pp. 590-608 (June 2001).				
DOTH		Rotenberg, Eric, et al., "Trace Processors", Proceedings of the 30th Annual IEEE/ACM International Symposium on Microarchitecture, pp. 138-148 (December 1, 1997).				
HOG		Vajapeyam, Sriram, et al., "Improving Superscalar Instruction Dispatch and Issue by Exploiting Dynamic Code Sequences", Proceedings of the 24th Annual International Symposium on Computer Architecture, pp. 1-12 (June 2 1997).	,			
			-			
	•					

Examiner Signature	Dant 1 Thr	m	Date Considered	8-10-	15

Based on PTO/SB/08B (08-03) as modified by Blakety, Solokoff, Taylor & Zafman (wtr) 08/11/2003. Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

<sup>\*</sup>Examinor: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>&#</sup>x27;Applicant's unique citation designation number.'Applicant is to place a check mark here if English language Translation is attached.